Fig. 1

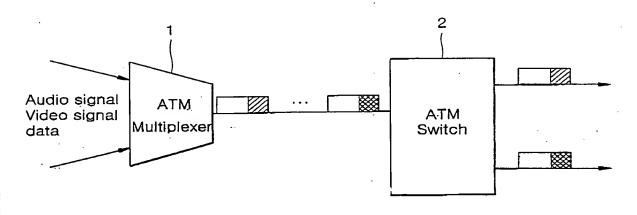
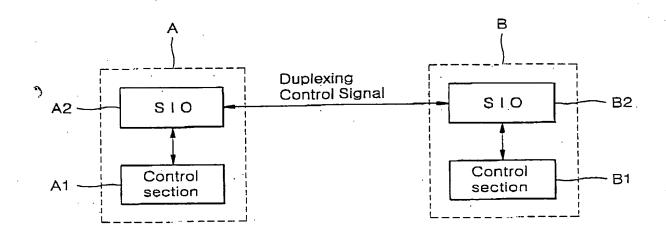


Fig. 2



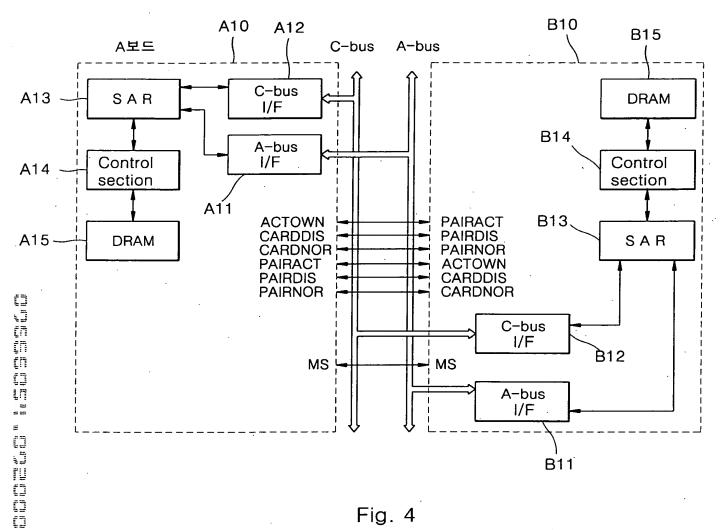


Fig. 4

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Header (5byte)			
Node	Slot	Port	
VP			$\neg \setminus$
VP			\neg
. VC			 \
. VC			7 \
Node	Slot	Port	payload
· VP			\neg
VP ·			\neg
VC			\neg /
VC			\Box /
- Connection Related information Band width, cell type etc			